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REVISION HISTORY:

V1.0 - 13/10/2016

NOTES:

These reduced schematics omit core SMPS and LPDDR2 circuitry only
V_CORE powers core SMPS which generates VDD_CORE
Route ringed signals as matched length 100R differential pairs
Route ringed signals as matched length 100K differential pairs.

R25 fitted, R24 not fitted on CM3L.

R25 fitted, R24 not fitted on CM3L.

R25 fitted, R24 not fitted on CM3L.

R25 fitted, R24 not fitted on CM3L.

BCM2837 will fail to boot from eMMC/SD if EMMC_DISABLE_N is LOW and will therefore fall back to booting from USB.